

FIG. 1A

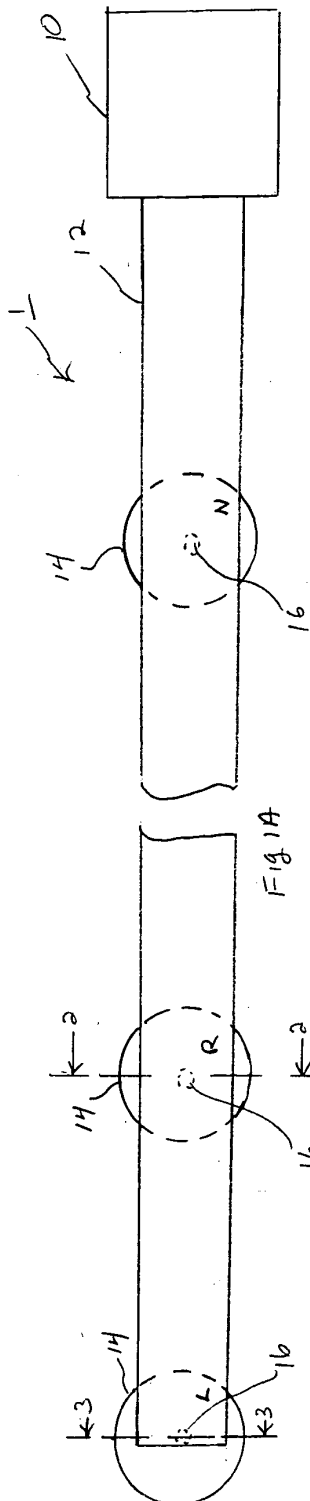


FIG. 1A

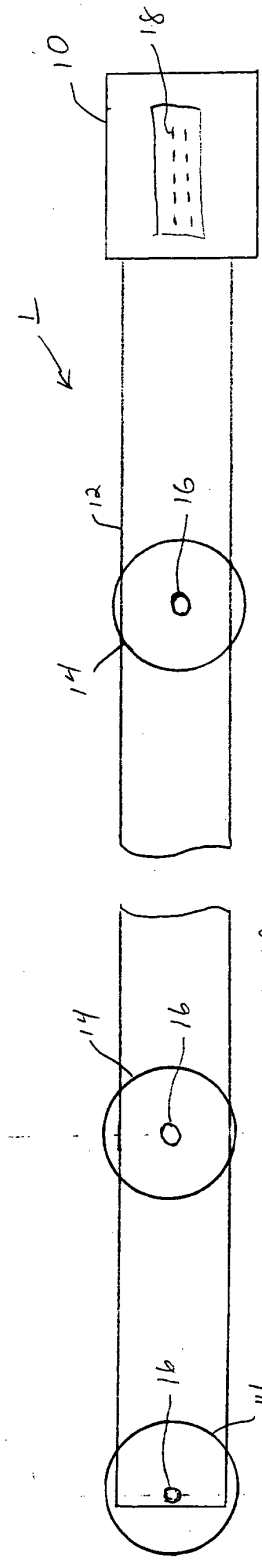


FIG. 1B

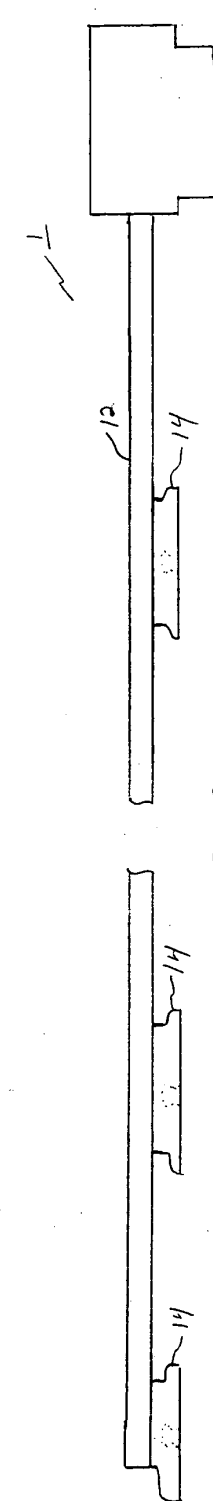
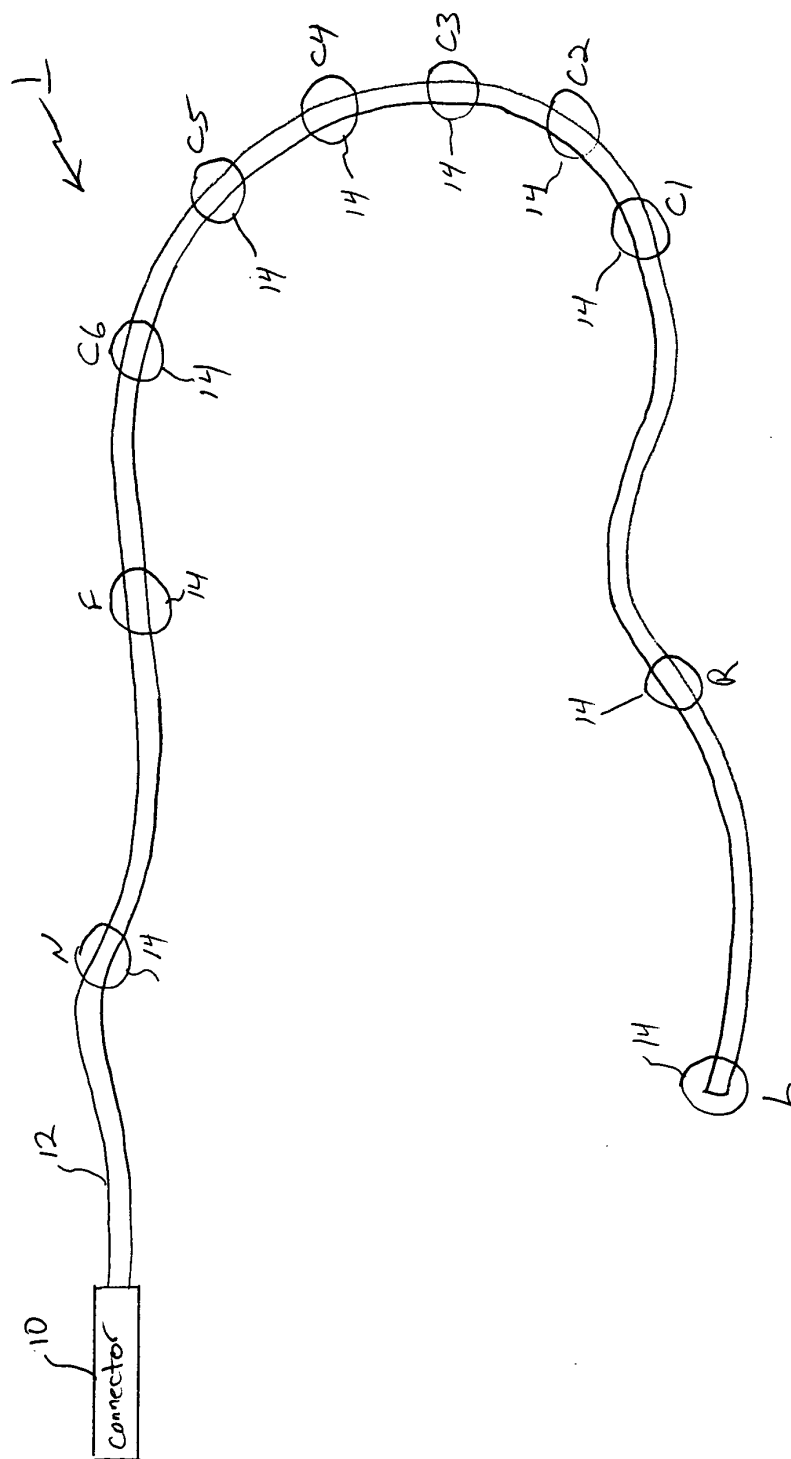
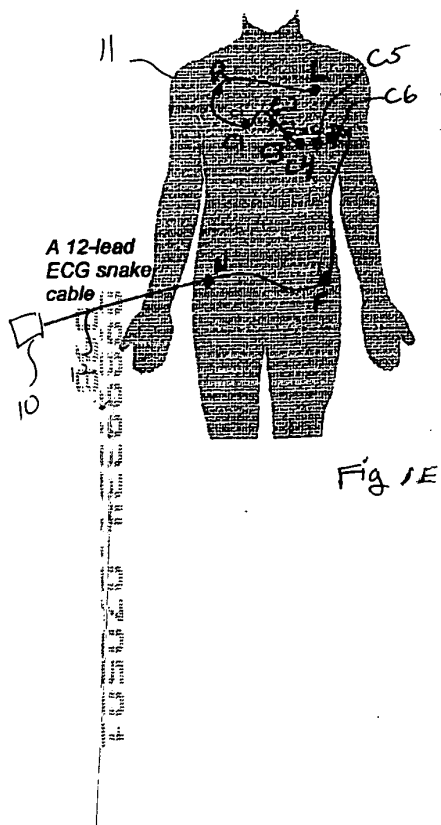
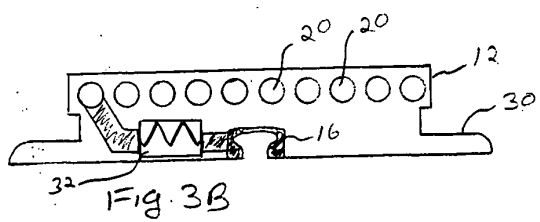
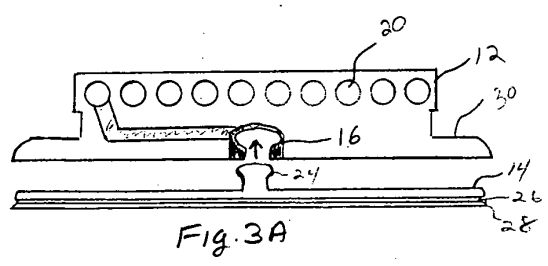
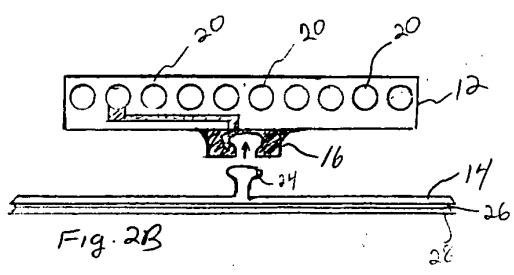
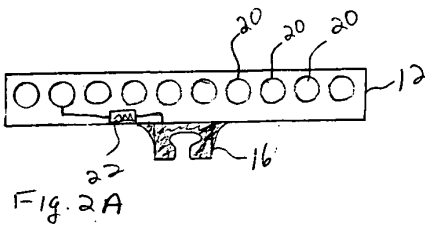


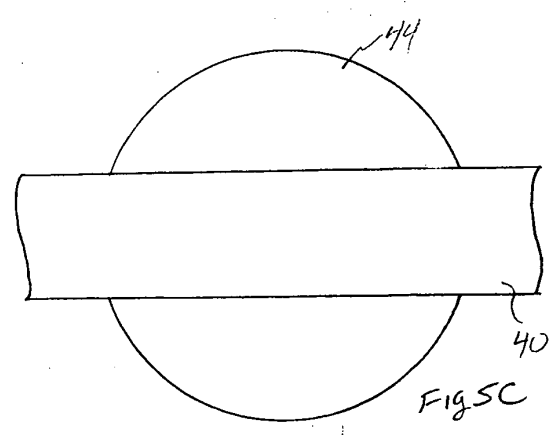
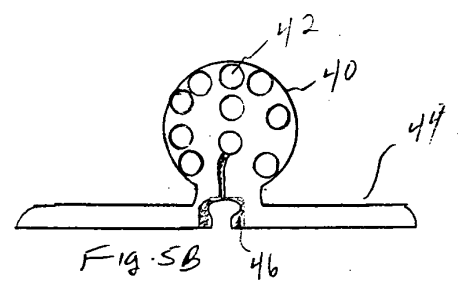
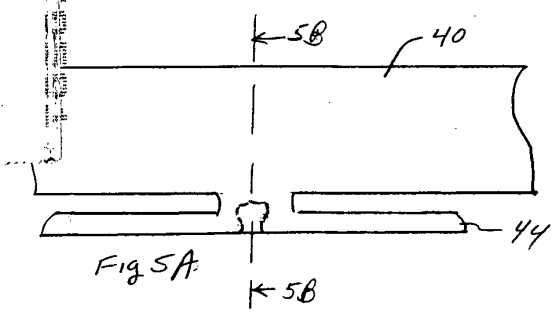
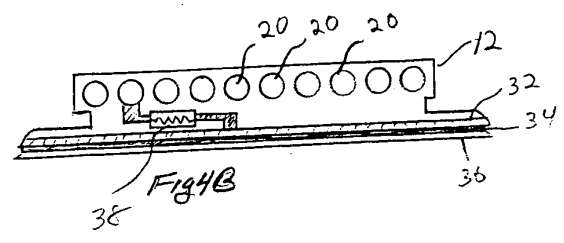
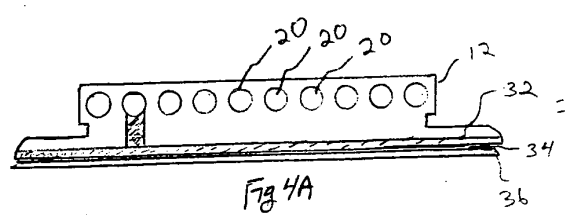
FIG. 1C

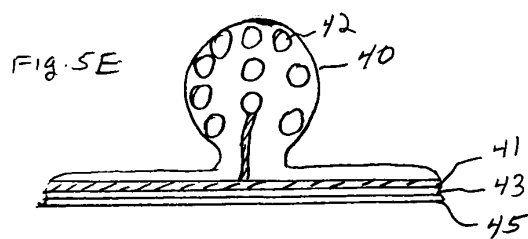
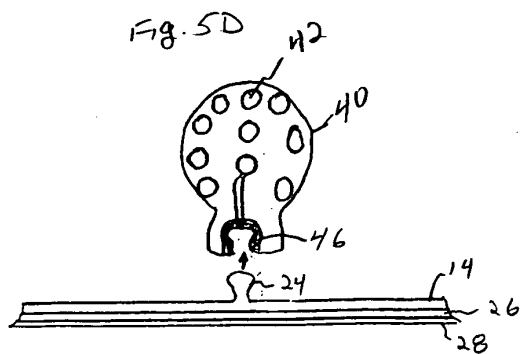
FIG. 1D











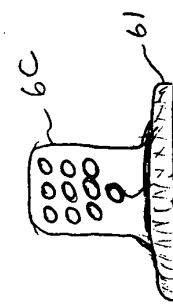
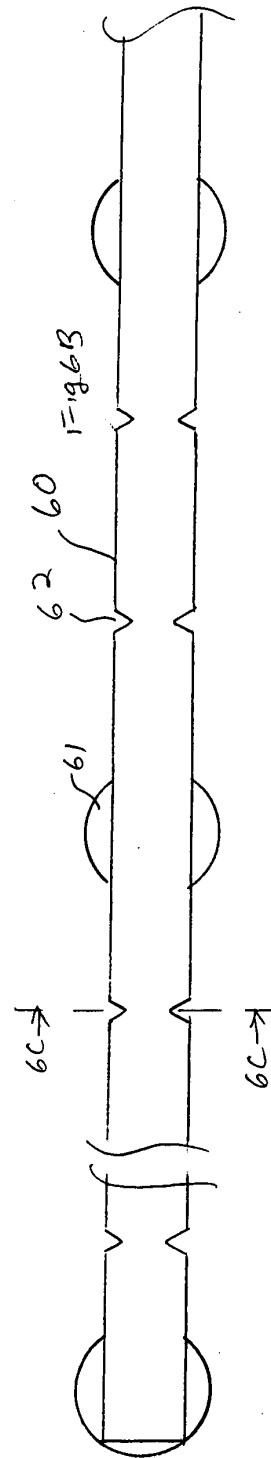
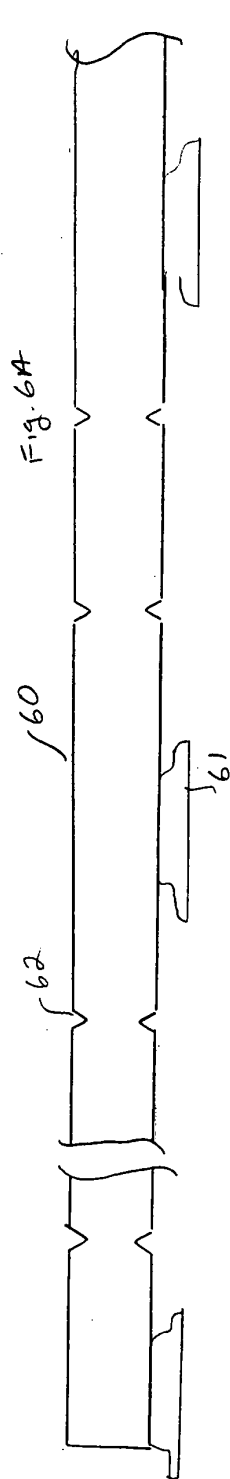


Fig. 6C

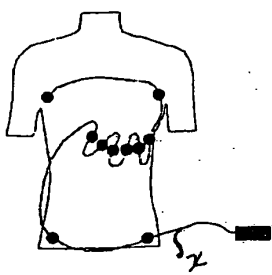


Fig 7A

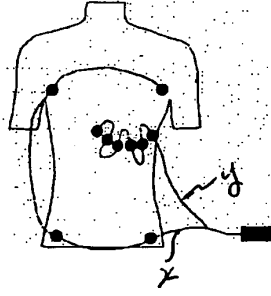


Fig 7B

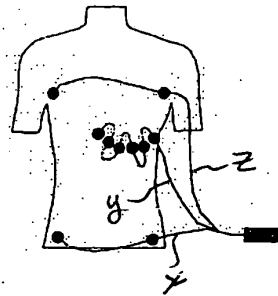


Fig 7C

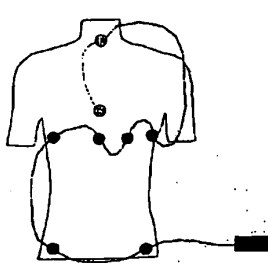


Fig 8A

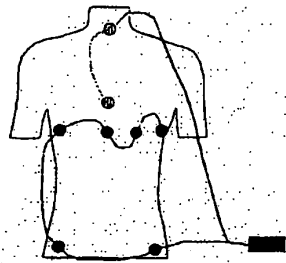


Fig 8B

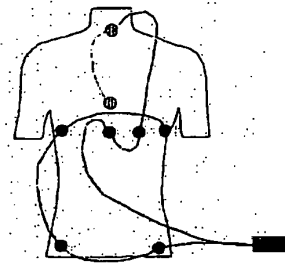


Fig 8C



FIG. 9 is a schematic diagram of a system 100, including a device 102, a component 100, a component 100, and a component 100.

Fig. 9

